

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	46	((yasuda.in. and shinichi.in.) or (fujita.in. and shinobu.in.)) and count\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/04/14 16:06
L2	10	1 and random\$5	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/04/14 16:07
L3	5	2 and count\$3.clm.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/04/14 16:07
L4	767	708/250-256.ccls. and @ad<"20020913"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/04/14 16:55
L5	365	4 and count\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/04/14 16:55
L6	287	5 and (latch\$3 or register\$1 or flip-flop\$1)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/04/14 16:56
L7	222	6 and clock\$1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/04/14 16:56
L8	104	count\$3.ti,ab,clm. and 7	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/04/14 17:05

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L9	16	5 and intensity and frequency	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/04/14 17:05
S1	756	708/250-256.ccls. and @ad<"20020913"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/05 16:33
S2	136	S1 and counter\$1 and (latch\$3 or flip-flop\$1 or (flip adj flop\$1))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/05 17:10
S3	116	S2 and clock\$1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/05 16:57
S4	4488	(yasuda.in. or shinichi.in. or fujita.in. or shinobu.in.) and random\$5	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/05 16:58
S5	1095	S4 and counter\$1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/05 16:58
S6	791	S5 and @ad<"20020913"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/05 16:58
S7	245	S6 and counter\$1 and (latch\$3 or flip-flop\$1 or (flip adj flop\$1))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/05 16:59

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S8	3	S7 and S1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/05 17:00
S9	0	(spectrum adj intensity) and S1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/05 17:02
S10	0	(spectrum adj intensity) and S1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/04/14 16:54
S11	41418	((random and number).ti. or (random and number).clm. or (random and number).ab.) and @ad<"20020913"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/05 17:02
S12	9016	S11 and counter\$1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/05 17:02
S13	2976	S12 and (latch\$3 or flip-flop\$1 or (flip adj flop\$1))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/05 17:02
S14	0	(spectrum adj intensity) and S13	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/05 17:02
S15	0	(spectrum adj intensity) and S13	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/05 17:07

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S16	0	(spectrum adj intensity) and S1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/05 17:10
S17	143	(counter\$1.ti. or counter\$1.ab. or counter\$1.clm.) and S1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/05 17:10
S18	105	S17 and clock\$1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/05 17:10
S19	50	S18 and (latch\$3 or flip-flop\$1 or (flip adj flop\$1))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/05 17:11
S20	54	("4545024" "4769777" "4799259" "4810975" "4853884" "4855690").PN. OR ("5007087"). URPN.	US-PGPUB; USPAT; USOCR	OR	ON	2006/10/05 17:23
S21	28	("3691472" "3790768" "4115657").PN. OR ("4355366"). URPN.	US-PGPUB; USPAT; USOCR	OR	ON	2006/10/05 17:27
S22	263	acquisition adj clock\$1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/05 17:31
S23	3	S22 and S1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/05 17:31
S24	119	counter\$1 and S22	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/04/14 16:06

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S25	3	S24 and S1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/05 17:31
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IEEE JNL IEEE Journal or Magazine

IET JNL IET Journal or Magazine

IEEE CNF IEEE Conference Proceeding

IET CNF IET Conference Proceeding

IEEE STD IEEE Standard

- ☐ 1. **Physical random number generator based on MOS structure after soft breakdown**
Yasuda, S.; Satake, H.; Tanamoto, T.; Ohba, R.; Uchida, K.; Fujita, S.;
Solid-State Circuits, IEEE Journal of
Volume 39, Issue 8, Aug. 2004 Page(s):1375 - 1377
Digital Object Identifier 10.1109/JSSC.2004.831480
[AbstractPlus](#) | [References](#) | Full Text: [PDF](#)(144 KB) IEEE JNL
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- ☐ 2. **Strong electron correlation effects in non-volatile electronic memory devices**
Inoue, I.H.; Rozenberg, M.J.; Yasuda, S.; Sanchez, M.J.; Yamazaki, M.; Manago, T.; Akinaga, H.; Takagi, H.; Akoh, H.; Tokura, Y.;
Non-Volatile Memory Technology Symposium, 2005
7-10 Nov. 2005 Page(s):6 pp.
Digital Object Identifier 10.1109/NVMT.2005.1541421
[AbstractPlus](#) | Full Text: [PDF](#)(521 KB) IEEE CNF
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- ☐ 3. **Si nanocrystal MOSFET with silicon nitride tunnel insulator for high-rate random number generation**
Ohba, R.; Matsushita, D.; Muraoka, K.; Yasuda, S.; Tanamoto, T.; Uchida, K.; Fujita, S.;
Emerging VLSI Technologies and Architectures, 2006. IEEE Computer Society Annual Symposium on
Volume 00, 2-3 March 2006 Page(s):6 pp.
Digital Object Identifier 10.1109/ISVLSI.2006.83
[AbstractPlus](#) | Full Text: [PDF](#)(536 KB) IEEE CNF
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- ☐ 4. **Physical random number generators for cryptographic application in mobile devices**
Yasuda, S.; Tanamoto, T.; Ohba, R.; Abe, K.; Nozaki, H.; Fujita, S.;
Solid-State Circuits Conference, 2005. ESSCIRC 2005. Proceedings of the 31st European
12-16 Sept. 2005 Page(s):399 - 402
Digital Object Identifier 10.1109/ESSCIR.2005.1541644
[AbstractPlus](#) | Full Text: [PDF](#)(258 KB) IEEE CNF
[Rights and Permissions](#)
- ☐ 5. **Si nanodevices for random number generating circuits for cryptographic security**
Fujita, S.; Uchida, K.; Yasuda, S.; Ohba, R.; Nozaki, H.; Tanamoto, T.;
Solid-State Circuits Conference, 2004. Digest of Technical Papers. ISSCC. 2004 IEEE International
15-19 Feb. 2004 Page(s):294 - 295 Vol.1
Digital Object Identifier 10.1109/ISSCC.2004.1332710
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6. **Narrow-channel-MOSFET having Si-dots for high-rate generation of random numbers**

Ohba, R.; Yasuda, S.; Tanamoto, T.; Uchida, K.; Fujita, S.;
Electron Devices Meeting, 2003. IEDM '03 Technical Digest. IEEE International
8-10 Dec. 2003 Page(s):31.3.1 - 31.3.4
Digital Object Identifier 10.1109/IEDM.2003.1269388
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7. **Ultra-small physical random number generators based on Si nanodevices for security systems and comparison to other large physical random number generators**

Yasuda, S.; Uchida, K.; Tanamoto, T.; Ohba, R.; Fujita, S.;
Nanotechnology, 2003. IEEE-NANO 2003. 2003 Third IEEE Conference on
Volume 2, 12-14 Aug. 2003 Page(s):531 - 534 vol. 2
Digital Object Identifier 10.1109/NANO.2003.1230964
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8. **Single-electron random-number generator (RNG) for highly secure ubiquitous computing applications**

Uchida, K.; Tanamoto, T.; Ohba, R.; Yasuda, S.; Fujita, S.;
Electron Devices Meeting, 2002. IEDM '02. Digest. International
8-11 Dec. 2002 Page(s):177 - 180
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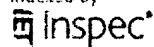
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IEEE CNF IEEE Conference Proceeding

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IEEE STD IEEE Standard

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1. **Rayleigh scattering influence on performance of 10 Gb/s optical receiver with Er-doped optical fiber preamplifier**
Henmi, N.; Aoki, Y.; Fujita, S.; Sunohara, Y.; Shikada, M.;
[Photonics Technology Letters, IEEE](#)
Volume 2, Issue 4, April 1990 Page(s):277 - 278
Digital Object Identifier 10.1109/68.53261
[AbstractPlus](#) | Full Text: [PDF](#)(160 KB) IEEE JNL
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2. **Physical random number generator based on MOS structure after soft breakdown**
Yasuda, S.; Satake, H.; Tanamoto, T.; Ohba, R.; Uchida, K.; Fujita, S.;
[Solid-State Circuits, IEEE Journal of](#)
Volume 39, Issue 8, Aug. 2004 Page(s):1375 - 1377
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3. **Si nanocrystal MOSFET with silicon nitride tunnel insulator for high-rate random number generation**
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5. **Si nanodevices for random number generating circuits for cryptographic security**
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[Solid-State Circuits Conference, 2004. Digest of Technical Papers. ISSCC. 2004 IEEE International](#)
15-19 Feb. 2004 Page(s):294 - 295 Vol.1
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8. **Silicon nitride trap memory with double tunnel junction**
Ohba, R.; Sugiyama, N.; Koga, J.; Fujita, S.;
[VLSI Technology, 2003. Digest of Technical Papers. 2003 Symposium on](#)
10-12 June 2003 Page(s):35 - 36
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8-11 Dec. 2002 Page(s):177 - 180
Digital Object Identifier 10.1109/IEDM.2002.1175807
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Inventor Information for 10/661593

Inventor Name	City	State/Country
YASUDA, SHINICHI	YOKOHAMA-SHI	JAPAN
FUJITA, SHINOBU	KAWASAKI-SHI	JAPAN

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Your Search was:

Last Name = FUJITA

First Name = SHINOBU

Application#	Patent#	Status	Date Filed	Title	Inventor Name
07918057	5319223	150	07/24/1992	HIGH ELECTRON MOBILITY TRANSISTOR	FUJITA, SHINOBU
09143106	6157047	150	08/28/1998	LIGHT EMMITTING SEMICONDUCTOR DEVICE USING NANOCRYSTALS	FUJITA, SHINOBU
09304343	6208000	150	05/04/1999	SEMICONDUCTOR ELEMENT HAVING CHARGE ACCUMULATING LAYER UNDER GATE ELECTRODE AND USING SINGLE ELECTRON PHENOMENON	FUJITA, SHINOBU
10235827	7111029	150	09/06/2002	RANDOM NUMBER GENERATING CIRCUIT	FUJITA, SHINOBU
10373874	Not Issued	71	02/27/2003	Random number generating device	FUJITA, SHINOBU
10661593	Not Issued	71	09/15/2003	Random number generator	FUJITA, SHINOBU
10761326	Not Issued	30	01/22/2004	Seed generating circuit, random number generating circuit, semiconductor integrated circuit, IC card, and information terminal equipment	FUJITA, SHINOBU
10902301	7001787	150	07/30/2004	ELECTRODE MANUFACTURING METHOD	FUJITA, SHINOBU
10919291	Not Issued	30	08/17/2004	Random number generating circuit, semiconductor integrated circuit, IC card and information terminal device	FUJITA, SHINOBU
10943973	7068069	150	09/20/2004	CONTROL CIRCUIT AND RECONFIGURABLE LOGIC BLOCK	FUJITA, SHINOBU
10997943	Not Issued	71	11/29/2004	Random number generating element	FUJITA, SHINOBU
11061951	Not Issued	41	02/17/2005	Nonvolatile memory for logic circuits	FUJITA, SHINOBU
11067672	Not Issued	30	03/01/2005	Random number test circuit, random number generation circuit, semiconductor integrated circuit, IC card and information terminal device	FUJITA, SHINOBU
11229502	Not Issued	30	09/20/2005	Switching element	FUJITA, SHINOBU

11234119	Not Issued	30	09/26/2005	Switching element, line-switching device and logic circuit	FUJITA, SHINOBU
11625307	Not Issued	25	01/20/2007	CARBON NANOTUBE TRANSISTOR HAVING LOW FRINGE CAPACITANCE AND LOW CHANNEL RESISTANCE	FUJITA, SHINOBU
11635590	Not Issued	17	12/08/2006	Random number test circuit	FUJITA, SHINOBU
11690125	Not Issued	19	01/01/0001	Memory Based Computation Systems and Methods of Using the Same	FUJITA, SHINOBU
60822229	Not Issued	20	08/11/2006	IMPACT OF GEOMETRY-DEPENDENT PARASITIC CAPACITANCES ON THE PERFORMANCE OF CNFET CIRCUITS	FUJITA, SHINOBU
60892110	Not Issued	20	02/28/2007	PROGRAMMABLE ANTI-FUSE BASED ON, E.G., ZNCDS MEMORY DEVICES FOR FPGA AND OTHER APPLICATIONS	FUJITA, SHINOBU

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First Name = SHINICHI

Application#	Patent#	Status	Date Filed	Title	Inventor Name
06207796	4323702	150	11/17/1980	PROCESS FOR RECOVERING A CARBOXYLIC ACID	YASUDA, SHINICHI
07243044	4910270	250	09/12/1988	COMPOSITION CONTAINING EPOXY RESIN, DI(METH)ACRYLATE, N-HETEROCYCLIC COMPOUND AND POLYAMINE	YASUDA, SHINICHI
07987817	5223304	150	12/09/1992	PROCESS FOR FABRICATING MAGNETIC DISKS	YASUDA, SHINICHI
08191543	Not Issued	166	02/04/1994	MANUFACTURING APPARATUS FOR MAGNETIC DISC AND MANUFACTURING METHOD FOR MAGNETIC DISC	YASUDA, SHINICHI
08653107	5631041	150	05/24/1996	MANUFACTURING METHOD FOR MAGNETIC DISC	YASUDA, SHINICHI
10568413	Not Issued	17	02/14/2006	Rotary atomization head painting device	YASUDA, SHINICHI
10661593	Not Issued	71	09/15/2003	Random number generator	YASUDA, SHINICHI
10919291	Not Issued	30	08/17/2004	Random number generating circuit, semiconductor integrated circuit, IC card and information terminal device	YASUDA, SHINICHI
11067672	Not Issued	30	03/01/2005	Random number test circuit, random number generation circuit, semiconductor integrated circuit, IC card and information terminal device	YASUDA, SHINICHI
11156582	Not Issued	89	06/21/2005	Semiconductor memory element and semiconductor memory device	YASUDA, SHINICHI
11165404	7184297	150	06/24/2005	SEMICONDUCTOR MEMORY DEVICE	YASUDA, SHINICHI
06130302	4314049	150	03/14/1980	POLYESTER RESIN COMPOSITION	YASUDA, SHINICHIRO
06309840	Not Issued	166	08/11/1980	TONER FOR DEVELOPING AN ELECTROSTATICALLY CHARGED IMAGE	YASUDA, SHINICHIRO
06332891	4387211	150	12/21/1981	PROCESS FOR PRODUCING NEW POLYESTER RESIN AND PRODUCT THEREOF	YASUDA, SHINICHIRO

<u>06576217</u>	Not Issued	166	02/02/1984	TONER FOR DEVELOPING AN ELECTRO-STATICALLY CHARGED IMAGE	YASUDA, SHINICHIRO
<u>06602587</u>	<u>4530894</u>	150	04/20/1984	COATED MAGNETIC TONER POWDER	YASUDA, SHINICHIRO
<u>06616981</u>	<u>4588668</u>	150	06/04/1984	A POLYESTER RESIN, ELECTROPHOTOGRAPHIC DEVELOPER COMPOSITION	YASUDA, SHINICHIRO
<u>06800081</u>	<u>4657837</u>	150	11/06/1985	TONER FOR DEVELOPING AN ELECTROSTATICALLY CHARGED IMAGE	YASUDA, SHINICHIRO
<u>06800268</u>	Not Issued	166	11/21/1985	RESIN BINDER FOR TONER COMPOSITION	YASUDA, SHINICHIRO
<u>06838382</u>	<u>4788122</u>	150	03/11/1986	PRODUCTION OF POLYESTER AND ELECTROPHOTOGRAPHIC TONER CONTAINING THE SAME	YASUDA, SHINICHIRO
<u>07127179</u>	<u>4845002</u>	150	12/01/1987	TONER FOR DEVELOPMENT OF ELECTROSTATICALLY CHARGED IMAGE	YASUDA, SHINICHIRO
<u>07204953</u>	<u>5154999</u>	150	06/09/1988	RESIN BINDER FOR TONER COMPOSITION	YASUDA, SHINICHIRO
<u>07241472</u>	Not Issued	161	09/07/1988	PRODUCTION OF POLYESTER AND ELECTROPHOTOGRAPHIC TONER CONTAINING THE SAME	YASUDA, SHINICHIRO
<u>07375497</u>	<u>4977054</u>	150	07/05/1989	DEVELOPER FOR ELECTROSTATIC IMAGE COMPRISING CARRIER	YASUDA, SHINICHIRO
<u>07407935</u>	<u>4986450</u>	150	09/15/1989	TONER CARTRIDGE	YASUDA, SHINICHIRO
<u>08282062</u>	<u>5481344</u>	150	07/21/1994	AUXILIARY DEVICE, CARTRIDGE AND APPARATUS FOR TONER SUPPLY	YASUDA, SHINICHIRO
<u>08405351</u>	<u>5655180</u>	250	03/16/1995	TONER CARTRIDGE WITH A ROTARY ELEMENT WHICH IS ATTACHABLE TO AND DETACHABLE FROM A DEVELOPING APPARATUS	YASUDA, SHINICHIRO
<u>08462887</u>	<u>5667099</u>	150	06/05/1995	METHOD AND APPARATUS FOR POWDER DISTRIBUTION	YASUDA, SHINICHIRO
<u>08844775</u>	<u>5811158</u>	150	04/22/1997	METHOD AND APPARATUS FOR ELECTROSTATIC POWDER COATING	YASUDA, SHINICHIRO
<u>08844776</u>	<u>5894989</u>	250	04/22/1997	ELECTROSTATIC POWDER COATING GUN	YASUDA, SHINICHIRO
<u>08908956</u>	<u>5955152</u>	250	08/08/1997	ELECTROSTATIC POWDER COATING METHOD	YASUDA, SHINICHIRO
<u>09134351</u>	<u>6202945</u>	150	08/14/1998	METHOD AND APPARATUS FOR ELECTROSTATIC POWDER COATING	YASUDA, SHINICHIRO

Inventor Search Completed: No Records to Display.

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